WEST Search History

Hide Items | Restore | Clear | Cancel

DATE: Monday, May 16, 2005

Hide?	Set Name	Query	Hit Count
		DB=USPT; PLUR=YES; OP=ADJ	
☐	L16	717/$.ccls. and L15	3
☐	L15	(state with graph$2 with software) and (test$3) and algorithm$2	19
☐	L14	(state with graph$2 with software) and (test$3 with software)	9
☐	L13	(state with graph$2 with software with travers$6)	2
		DB=PGPB; PLUR=YES; OP=ADJ	
☐	L12	l10 and L11	54
☐	L11	717/$.ccls.	3486
☐	L10	L9 and ((different or various) with (algorithm$2 or test$5) with software)	574
☐	L9	(test$6 or debug$6) with software	9971
		DB=USPT,USOC,EPAB,JPAB,DWPI,TDBD; PLUR=YES; OP=ADJ	
☐	L8	random walk algorithm	7
☐	L7	L5 and (random with algorithm$2)	2
☐	L6	L5 and (multiple with algorithm$2)	0
☐	L5	L2 and L4	65
☐	L4	717/$.ccls.	4966
☐	L3	L2 and (state with graph$2)	17
☐	L2	L1 and ((different or various) with (algorithm$2 or test$5) with software)	800
☐	L1	(test$6 or debug$6) with software	18635

END OF SEARCH HISTORY

Results for "((test<in>ab) <and> (software<in>ab) <and> (algorithm<in>ab)<..."

e-mail

Your search matched **19** of **854** documents.

A maximum of **100** results are displayed, **25** to a page, sorted by **Relevance** in **Descending** order.

» View Session History

» New Search

Modify Search

((test<in>ab) <and> (software<in>ab) <and> (algorithm<in>ab)<and>(random<in>

Check to search only within this results set

Display Format: Citation Citation & Abstract

» Key

IEEE JNL	IEEE Journal or Magazine
IEE JNL	IEE Journal or Magazine
IEEE CNF	IEEE Conference Proceeding
IEE CNF	IEE Conference Proceeding
IEEE STD	IEEE Standard

Select Article Information

1. **Genetic algorithms for dynamic test data generation**
Michael, C.C.; McGraw, G.E.; Schatz, M.A.; Walton, C.C.;
Automated Software Engineering, 1997. Proceedings., 12th IEEE International Confere
1-5 Nov. 1997 Page(s):307 - 308
AbstractPlus | Full Text: PDF(180 KB) IEEE CNF

2. **Methods for identification of telemetering random errors variances in electric po**
Gamm, A.Z.; Grishin, Y.A.; Glazunova, A.M.; Kolosok, I.N.;
Power System Technology, 1998. Proceedings. POWERCON '98. 1998 International C
Volume 2, 18-21 Aug. 1998 Page(s):1255 - 1259 vol.2
AbstractPlus | Full Text: PDF(432 KB) IEEE CNF

3. **Fault simulation and test algorithm generation for random access memories**
Chi-Feng Wu; Chih-Tsun Huang; Kuo-Liang Cheng; Cheng-Wen Wu;
Computer-Aided Design of Integrated Circuits and Systems, IEEE Transactions on
Volume 21, Issue 4, April 2002 Page(s):480 - 490
AbstractPlus | References | Full Text: PDF(380 KB) IEEE JNL

4. **Laser programmable redundancy and yield improvement in a 64K DRAM**
Smith, R.T.; Chlipala, J.D.; Bindels, J.F.M.; Nelson, R.G.; Fischer, F.H.; Mantz, T.F.;
Solid-State Circuits, IEEE Journal of
Volume 16, Issue 5, Oct 1981 Page(s):506 - 514
AbstractPlus | Full Text: PDF(1408 KB) IEEE JNL

5. **Synthesis of multivariate Gaussian random processes with a preassigned covari**
Eby, E.;
Information Theory, IEEE Transactions on
Volume 16, Issue 6, Nov 1970 Page(s):773 - 776
AbstractPlus | Full Text: PDF(632 KB) IEEE JNL

6. **Fault coverage analysis of RAM test algorithms**
Riedel, M.; Rajski, J.;
VLSI Test Symposium, 1995. Proceedings., 13th IEEE
30 April-3 May 1995 Page(s):227 - 234
AbstractPlus | Full Text: PDF(576 KB) IEEE CNF

7. **A cryptographically secure EW database with selective random access**

Ikram, N.; Shepherd, S.J.;
MILCOM 97 Proceedings
Volume 3, 2-5 Nov. 1997 Page(s):1407 - 1411 vol.3

AbstractPlus | Full Text: PDF(944 KB) IEEE CNF

8. Determining optimal testing times for Markov chain usage models [software testi
Semmel, G.S.; Linton, D.G.;
Southeastcon '98. Proceedings. IEEE
24-26 April 1998 Page(s):1 - 4

AbstractPlus | Full Text: PDF(284 KB) IEEE CNF

9. Rapid Image registration for 3D ultrasound compounding
Kucker, J.F.; Carson, P.L.; LeCarpentier, G.L.; Fowlkes, J.B.; Meyer, C.R.;
Ultrasonics Symposium, 2000 IEEE
Volume 2, 22-25 Oct. 2000 Page(s):1585 - 1588 vol.2

AbstractPlus | Full Text: PDF(428 KB) IEEE CNF

10. SOCRATES on IP router fault detection
Hao, R.; Lee, D.; Sinha, R.K.;
Global Telecommunications Conference, 2000. GLOBECOM '00. IEEE
Volume 3, 27 Nov.-1 Dec. 2000 Page(s):1578 - 1582 vol.3

AbstractPlus | Full Text: PDF(412 KB) IEEE CNF

11. An efficient BIST method for testing of embedded SRAMs
Tehranipour, M.H.; Navabi, Z.; Fakhraie, S.M.;
Circuits and Systems, 2001. ISCAS 2001. The 2001 IEEE International Symposium on
Volume 5, 6-9 May 2001 Page(s):73 - 76 vol. 5

AbstractPlus | Full Text: PDF(288 KB) IEEE CNF

12. A P1500 compliant BIST-based approach to embedded RAM diagnosis
Appello, D.; Corno, F.; Giovinetto, M.; Rebaudengo, M.; Sonza Reorda, M.;
Test Symposium, 2001. Proceedings. 10th Asian
19-21 Nov. 2001 Page(s):97 - 102

AbstractPlus | Full Text: PDF(661 KB) IEEE CNF

13. Software generation of address-event-representation for Interchip images comm
Linares-Barranco, A.; Jimnez, G.; Civit, A.; Sevillano, J.L.; Paz, R.;
IECON 02 [Industrial Electronics Society, IEEE 2002 28th Annual Conference of the]
Volume 3, 5-8 Nov. 2002 Page(s):1915 - 1919 vol.3

AbstractPlus | Full Text: PDF(415 KB) IEEE CNF

14. Software-based weighted random testing for IP cores in bus-based programmab
Iyer, M.K.; Kwang-Ting Cheng;
VLSI Test Symposium, 2002. (VTS 2002). Proceedings 20th IEEE
28 April-2 May 2002 Page(s):139 - 144

AbstractPlus | Full Text: PDF(313 KB) IEEE CNF

15. PPMexe: PPM for compressing software
Drinic, M.; Kirovski, D.;
Data Compression Conference, 2002. Proceedings. DCC 2002
2-4 April 2002 Page(s):192 - 201

AbstractPlus | Full Text: PDF(376 KB) IEEE CNF

16. Environment for random and sinusoidal vibration test control of an inverter-fed e shaker
Stefanello, M.; Carati, E.G.;
Industrial Electronics, 2003. ISIE '03. 2003 IEEE International Symposium on

Volume 2, 9-11 June 2003 Page(s):1093 - 1098 vol. 2

AbstractPlus | Full Text: PDF(669 KB) IEEE CNF

☐ **17. FPGA implementation of image watermarking algorithm for a digital camera**
Hyun Lim; Soon-Young Park; Seong-Jun Kang; Wan-Hyun Cho;
Communications, Computers and signal Processing, 2003. PACRIM. 2003 IEEE Pacifi on
Volume 2, 28-30 Aug. 2003 Page(s):1000 - 1003 vol.2

AbstractPlus | Full Text: PDF(429 KB) IEEE CNF

☐ **18. Measurement of extreme execution times for software**
Jones, B.F.; Wegener, J.;
Real-Time Systems (Digest No. 1998/306), IEE Colloquium on
21 April 1998 Page(s):4/1 - 4/5

AbstractPlus | Full Text: PDF(308 KB) IEE CNF

☐ **19. Searching protection relay response time extremes using genetic algorithm-soft optimization**
Alander, J.T.; Mantere, T.; Moghadampour, G.; Matila, J.;
Advances in Power System Control, Operation and Management, 1997. APSCOM-97. International Conference on (Conf. Publ. No. 450)
Volume 1, 11-14 Nov. 1997 Page(s):95 - 99 vol.1

AbstractPlus | Full Text: PDF(440 KB) IEE CNF

Terms used **random test software algorithm** Found **8** of **154,226**

Sort results by: relevance

Display results: expanded form

Save results to a Binder

Search Tips

Open results in a new window

Try an Advanced Search

Try this search in The ACM Guide

Results 1 - 8 of 8

Relevance scale

1 Efficient Delaunay triangulation using rational arithmetic

Michael Karasick, Derek Lieber, Lee R. Nackman

January 1991 **ACM Transactions on Graphics (TOG)**, Volume 10 Issue 1

Full text available: pdf(1.33 MB) Additional Information: full citation, abstract, references, citings, index terms

Many fundamental tests performed by geometric algorithms can be formulated in terms of finding the sign of a determinant. When these tests are implemented using fixed precision arithmetic such as floating point, they can produce incorrect answers; when they are implemented using arbitrary-precision arithmetic, they are expensive to compute. We present adaptive-precision algorithms for finding the signs of determinants of matrices with integer and rational elements. These algorithms were dev ...

2 Bioinformatics: A Markov Random Field model of microarray gridding

Mathias Katzer, Franz Kummert, Gerhard Sagerer

March 2003 **Proceedings of the 2003 ACM symposium on Applied computing**

Full text available: pdf(769.06 KB) Additional Information: full citation, abstract, references

DNA microarray hybridisation is a popular high through-put technique in academic as well as industrial functional genomics research. In this paper we present a new approach to automatic grid segmentation of the raw fluorescence microarray images by Markov Random Field (MRF) techniques. The main objectives are applicability to various types of array designs and robustness to the typical problems encountered in microarray images, which are contaminations and weak signal.We briefly introduce microa ...

3 Collective steropsis on the hypercube

R. Battiti

January 1989 **Proceedings of the third conference on Hypercube concurrent computers and applications - Volume 2**

Full text available: pdf(522.17 KB) Additional Information: full citation, abstract, references, citings, index terms

A cooperative algorithm for extracting disparity information from stereo image pairs has been implemented on the NCUBE hypercube computer. Software is written in C-langauge, using communication routines of the "Crystalline Operating System" CrOSIII designed at Caltech within the Caltech Concurrent Computation Program. Some tests have been done using Julesz's random-dot stereograms. Although the software is reasonably versat ...

4 Associative/parallel processors for searching very large textual data bases

R. M. Bird, J. C. Tu, R. M. Worthy

January 1977 **Proceedings of the 3rd workshop on Computer architecture : Non-numeric processing**, Volume 9 , 12 , 6 Issue 2 , 1 , 2

Full text available: pdf(532.16 KB) Additional Information: full citation, abstract, citings, index terms

This paper describes an approach to solving a major problem in the information processing sciences— that of searching very large (5-50 billion characters) data bases of unstructured free-text for random queries within a reasonable time and at an affordable price. The need by information specialists and knowledge workers for large, fast low-cost text and document retrieval systems is growing rapidly. Conventional approaches to the problem have usually depended upon expensive ...

5 Optimizing for reduced code space using genetic algorithms

Keith D. Cooper, Philip J. Schielke, Devika Subramanian

May 1999 **ACM SIGPLAN Notices , Proceedings of the ACM SIGPLAN 1999 workshop on Languages, compilers, and tools for embedded systems**, Volume 34 Issue 7

Full text available: pdf(977.31 KB) Additional Information: full citation, abstract, references, citings, index terms

Code space is a critical issue facing designers of software for embedded systems. Many traditional compiler optimizations are designed to reduce the execution time of compiled code, but not necessarily the size of the compiled code. Further, different results can be achieved by running some optimizations more than once and changing the order in which optimizations are applied. Register allocation only complicates matters, as the interactions between different optimizations can cause more spill c ...

6 High performance TCP in ANSNET

Curtis Villamizar, Cheng Song

October 1994 **ACM SIGCOMM Computer Communication Review**, Volume 24 Issue 5

Full text available: pdf(1.41 MB) Additional Information: full citation, abstract, citings, index terms

This report concentrates on specific requirements and goals of the research networks supported by ANSNET, but applies to any TCP dominated high speed WAN and in particular those striving to support high speed end-to-end flows. Measurements have been made under conditions intended to better understand performance barriers imposed by network equipment queueing capacities and queue drop strategies.The IBM RS/6000 based routers currently supporting ANSNET performed very well in these tests. Measurem ...

7 Integrating solid image capability into a general purpose calligraphic graphics package

G. Laib, R. Puk, G. Stowell

July 1980 **ACM SIGGRAPH Computer Graphics , Proceedings of the 7th annual conference on Computer graphics and interactive techniques**, Volume 14 Issue 3

Full text available: pdf(904.40 KB) Additional Information: full citation, abstract, references, citings, index terms

Raster scanned graphics terminals provide several features not found in standard line drawing displays. Among them are area fill and an extensive color palette. Hardware support for such functions is becoming cost effective and available in a variety of forms. What is now needed is high level, device independent software that assists in the generation of and interaction with these terminals. To accomplish this, a project has been undertaken jointly by Sandia Laboratories, Purdue University, ...

8 Fast hardware random number generator for the Tausworthe sequence

Meir Barel

March 1983 **Proceedings of the 16th annual symposium on Simulation**

Full text available: pdf(482.42 KB) Additional Information: full citation, abstract, references, citings, index terms

Many simulation programs require m-dimensional uniformly distributed random numbers. A linear recurrence modulo two generator, based on N-bits and producing L-bit numbers ($L \leqslant N$), according to Tausworthe theory, may yield a sequence of m-tuples uniformly distributed

in m &equil; (N/L) dimensions. When using software computing algorithms on a binary computer, for large N (e.g. N &equil; 159), the generation speed is for many purposes too slow. To overcome this disadvantage we present a ne ...

Results 1 - 8 of 8